

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>				
<b>Filing Date:</b>				
<b>Title of Invention:</b>	SEMICONDUCTOR CHIP MOUNTED INTERPOSER, SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP INTERPOSER FABRICATION METHOD, BARE CHIP MOUNTED INTERPOSER, AND INTERPOSER SHEET			
<b>First Named Inventor:</b>	Moriyoshi NAKASHIMA			
<b>Filer:</b>	Mark D. Saralino/Christine Arndt			
<b>Attorney Docket Number:</b>	KOMOP0113US			
Filed as Large Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
National Stage Fee	1631	1	300	300
Natl Stage Search Fee - Report provided	1642	1	400	400
National Stage Exam - all other cases	1633	1	200	200
<b>Pages:</b>				
<b>Claims:</b>				
Independent claims in excess of 3	1614	1	200	200
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>1100</b>